

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Reissue Application Of: Minoru FUKUDA, et al.
For: MEMORY CONFIGURATION OF A COMPOSITE
MEMORY DEVICE
Reissue Application No.: not yet known
Reissue Application Filing Date: concurrently herewith
Original Patent No.: 6,335,883
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1185 Avenue of the Americas
New York, New York 10036

Mail Stop Reissue Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Statement Pursuant To 37 CFR 1.173 (c)

Explanation of Support

Reissue claims 1, 5, 13 and 17 differ from the original claims by reversing the references to a first and a second array in the last wherein clause, in order to fully conform to the disclosed embodiments. Non-limiting support for the changes is found at: (a) col. 3, referring at lines 55-57 to array 12A storing control programs and at lines 59-62 to array 12B storing data; and (b) col. 5 referring at lines 2-4 to block unit erasing mode (of array 12B) in which a single block or plural blocks are erased, and at lines 14-26 further explaining a mode of erasing plural blocks.

Reissue claims 8, 12 and 20 differ from the original claims by reversing the references to a first and a second area in the last wherein clause. Non-limiting support can be found in the same

